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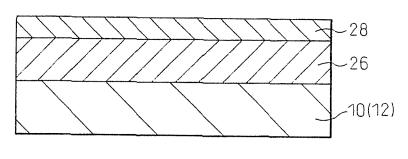
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(54) Title: EXTERNAL PALLADIUM PLATING STRUCTURE OF SEMICONDUCTOR COMPONENT AND SEMICONDUCTOR DEVICE MANUFACTURING METHOD

(54) 発明の名称: 半導体部品の外装パラジウムめっき構造及び半導体装置の製造方法



(57) Abstract: In an external plating structure of a semiconductor package, a Pd or Pd alloy film is used as a material replacing the conventional solder plating as a soldering metal. The highly reliable semiconductor component external plating structure is provided without causing problems, such as short-circuiting, due to whiskers, etc. between terminals. In the external plating structure, on the surfaces of the external connecting terminals (10, 12)

of the semiconductor component using copper or copper alloy material. Pd or Pd alloy (26) is used, replacing the conventional solder plating as a soldering metal. In the case of forming a plating film having a thickness of 0.3μ m or less, plating is performed between the material and the plated Pd or Pd alloy layer, without having a base layer or an intermediate metal layer in between. On the plating film, depending on a case, an Au or Au alloy plating (28) having a thickness of 0.1μ m or less is further formed.

(57) 要約: 半導体パッケージの外装めっき構造において、ロウ付け金属としての従来のはんだめっきに代わる材料として、Pd又はPd合金皮膜を用い、端子間におけるウィスカー等による短絡等の問題を生ずることなく、信頼性の高い半導体部品の外装めっき構造を提供する。本発明の外装めっき構造では、銅又は銅合金系素材を使用した半導体部品の外部接続端子(10、12)の表面に、ロウ付け金属としての従来のはんだめっきに代わる材料として、Pd又はPd合金(26)を用い、0、3 μ m以下の厚さのめっき皮膜を形成する場合に、前記素材とめっきしたPd又はPd合金層との間に、下地層又は中間金属層を介在させることなく、前記めっきを施す、また場合によっては、更にその上に0、1 μ m以下のAu又はAu合金めっき(28)を施す。



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添付公開書類:

一 国際調査報告書

2文字コード及び他の略語については、定期発行される各*PCT*ガゼットの巻頭に掲載されている「コードと略語のガイダンスノート」を参照。

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PCT/JP2005/009286

A. CLASSIFIC	CATION OF SUBJECT MATTER	101/05	2005/009286	
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Jitsuyo Kokai J	itsuyo Shinan Koho 1971-2005 T	itsuyo Shinan Toroku Koho Oroku Jitsuyo Shinan Koho	1996-2005 1994-2005	
	pase consulted during the international search (name of	data base and, where practicable, search t	erms used)	
	NTS CONSIDERED TO BE RELEVANT		***************************************	
Category*	Citation of document, with indication, where a	·	Relevant to claim No.	
<u>А</u>	JP 4-115558 A (Shinko Electr Ltd.), 16 April, 1992 (16.04.92), Page 2, upper left column, l lower left column, line 6 & EP 0474499 A2	ric Industries Co., ine 4 to page 4,	1 - 4 <u>5 , 6</u>	
У	JP 11-317487 A (Nissan Motor 16 November, 1999 (16.11.99) Par. Nos. [0024] to [0033] (Family: none)	5,6		
A	JP 11-204713 A (Sony Corp.), 30 July, 1999 (30.07.99), Full text & US 6150712 A		1-4	
lhirther do	comments are listed in the continuation of $\operatorname{Box} C$.	See patent family annex.		
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